

30V 6.3mohm N-channel Trench MOSFET AKT3040Q

Description:

This N channel MOSFET has been designed to low on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, special for high efficiency power management applications.

Features:

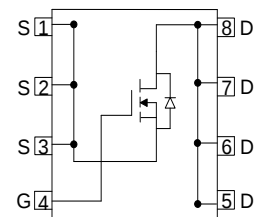
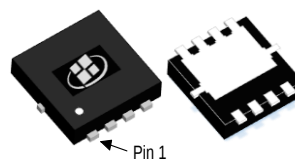
- LOW $R_{DS(ON)}$
- RoHS compliant
- Halogen-free

Applications:

- Battery Management System
- Motor Drivers
- DC-DC Converter

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	30	V
$R_{DS(ON)}, \max @ V_{GS} = 10 \text{ V}$	6.3	m Ω
I_D	36	A



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKT3040Q	PDFN3X3	AKT3040Q	Tape Reel	See the detail package information

Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain - Source Voltage	30	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^(Note 1)	63	A
	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^(Note 2)	36	A
	Drain Current - Continuous ($T_C = 100^\circ\text{C}$)	40	A
I_{DM}	Drain Current - Pulsed ^(Note 3)	144	A
V_{GS}	Gate - Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy ^(Note 4)	72	mJ
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	41.5	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady State	3	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady State ^(Note 5)	60	$^\circ\text{C/W}$

Notes:

1. The max drain current rating is silicon limited
2. The max drain current rating is package limited
3. Repetitive Rating: Pulse width limited by maximum junction temperature
4. $L = 0.5\text{ mH}$, $V_{DD} = 15\text{ V}$, $I_{AS} = 17\text{ A}$, $R_g = 25\text{ }\Omega$, Starting $T_J = 25\text{ }^\circ\text{C}$
5. Mount on minimum PCB layout

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Static Characteristics

$V_{(BR)DSS}$	Drain - Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V},$			1	μA
I_{GSS}	Gate Leakage Current	$V_{GS} = \pm 12\text{ V}, V_{DS} = 0\text{ V}$			± 100	nA
$V_{GS(th)}$	Gate Threshold voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.0	1.5	2.0	V
$R_{DS(ON)}$	Drain - Source on - state resistance	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		3.5	6.3	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 10\text{ A}$		5.9	10.0	

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$		1830		pF
C_{oss}	Output Capacitance			235		pF
C_{rss}	Reverse Transfer Capacitance			195		pF
R_g	Gate Resistance	$f = 1\text{ MHz}$		3		Ω

Switching Characteristics

$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 20\text{ V}, R_L = 1\text{ }\Omega,$ $V_{GS} = 10\text{ V}, R_G = 3.3\text{ }\Omega$		7		ns
t_r	Rise Time			33		ns
$t_{d(off)}$	Turn Off Delay Time			33		ns
t_f	Fall Time			14		ns
Q_g	Total Gate Charge	$V_{DD} = 15\text{ V}, I_D = 30\text{ A},$ $V_{GS} = 10\text{ V}$		35		nC
Q_{gs}	Gate - Source Charge			5		nC
Q_{gd}	Gate - Drain Charge			9		nC

Drain - Source Diode Characteristics and Maximum Ratings

I_S	Maximum Continuous Body - Diode Forward Current			36	A
I_{SM}	Maximum Pulsed Body - Diode Forward Current			144	A
V_{SD}	Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 30\text{ A}$		0.84	V
t_{rr}	Reverse recovery time	$V_{DD} = 20\text{ V}, I_D = 20\text{ A},$ $di/dt = 100\text{ A}/\mu\text{s}$		18	ns
Q_{rr}	Reverse recovery charge			10	nC
I_{rrm}	Peak Reverse Recovery Current			0.95	A

Electrical Characteristics Diagrams

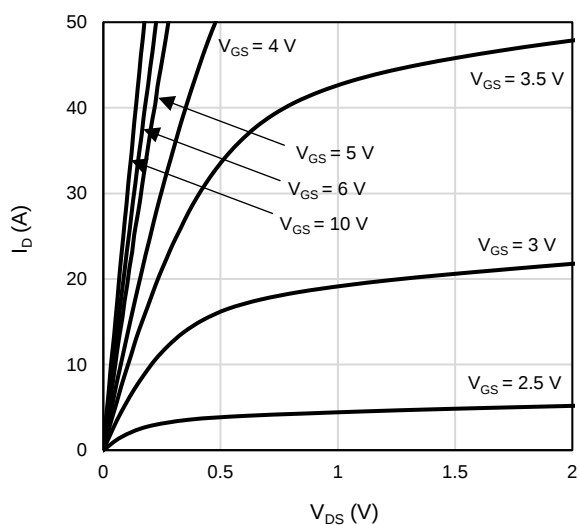


Figure 1: On-Region Characteristics

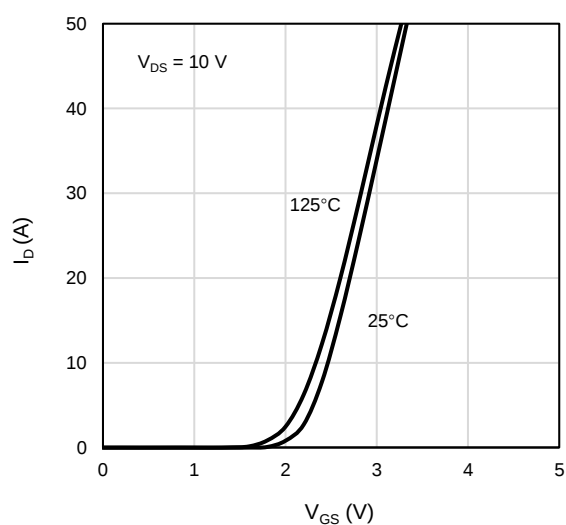


Figure 2: Transfer Characteristics

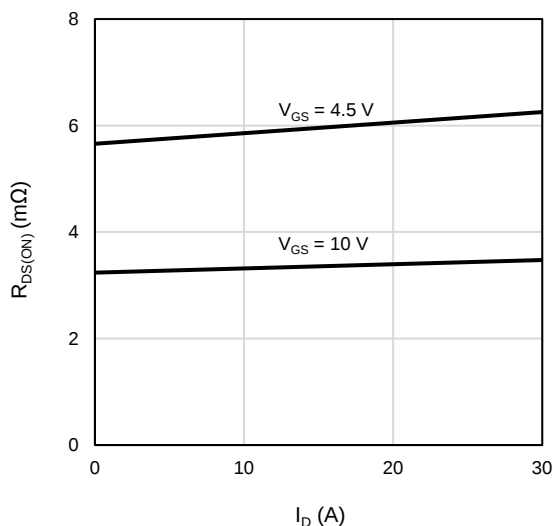


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

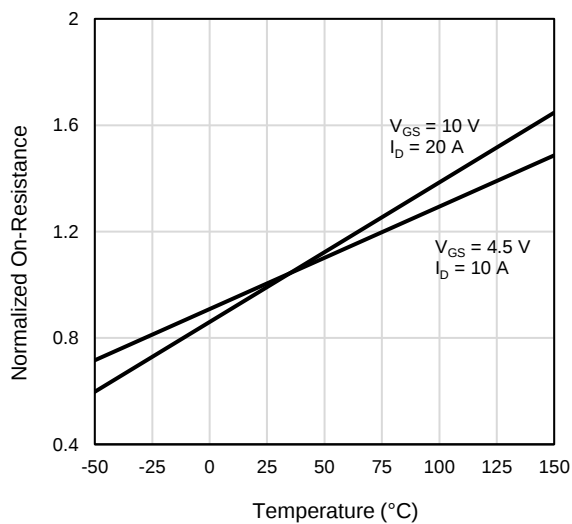


Figure 4: On-Resistance vs. Junction Temperature

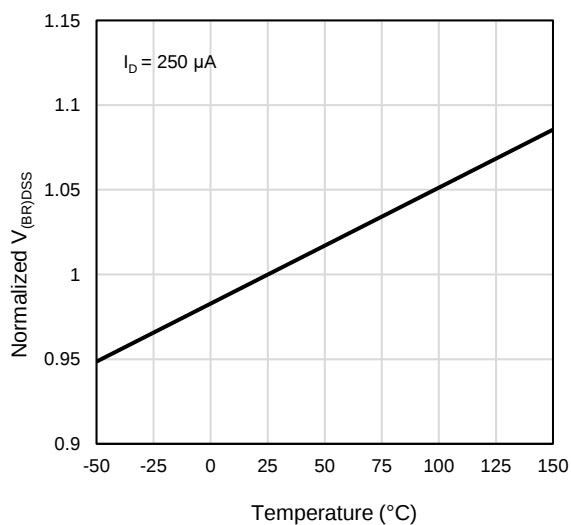


Figure 5: Breakdown Voltage vs. Junction Temperature

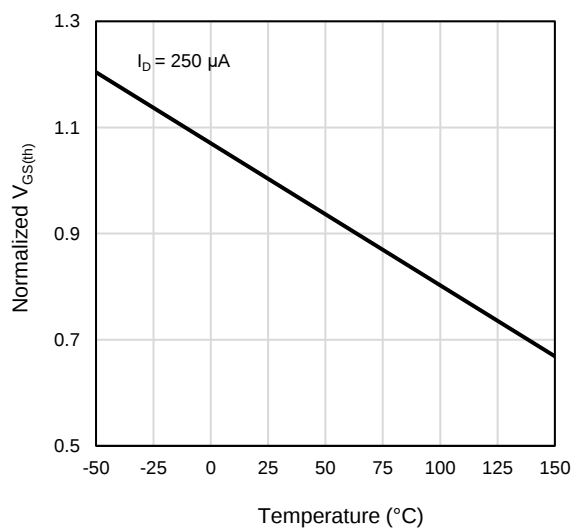


Figure 6: Threshold Voltage vs. Junction Temperature

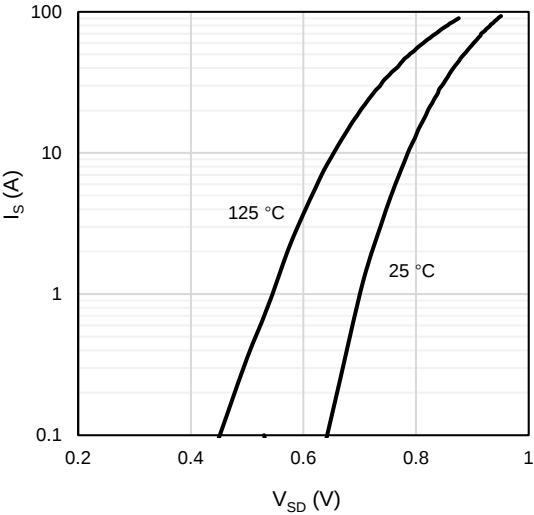


Figure 7: Body-Diode Characteristics

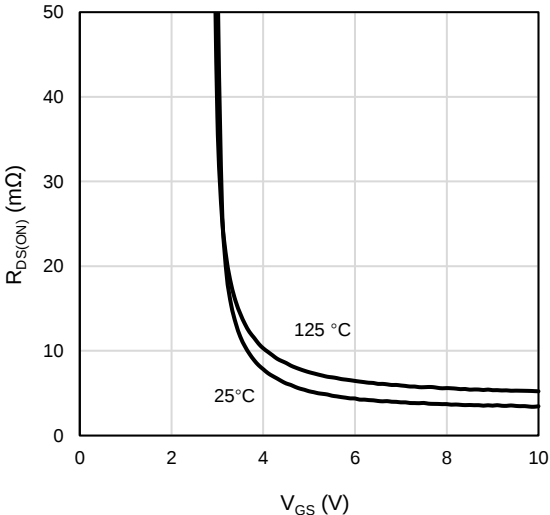


Figure 8: On-Resistance vs. Gate-Source Voltage

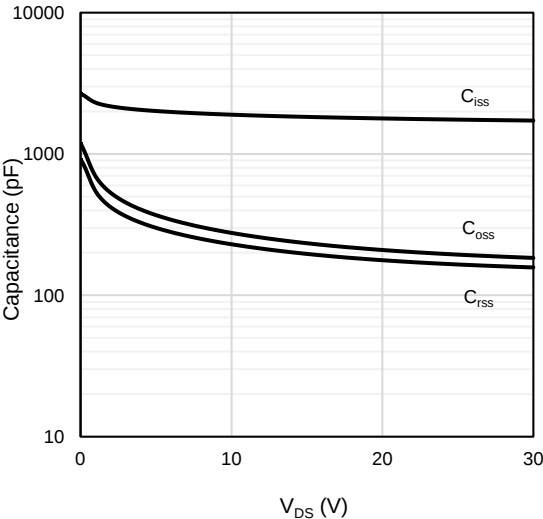


Figure 9: Capacitance Characteristics

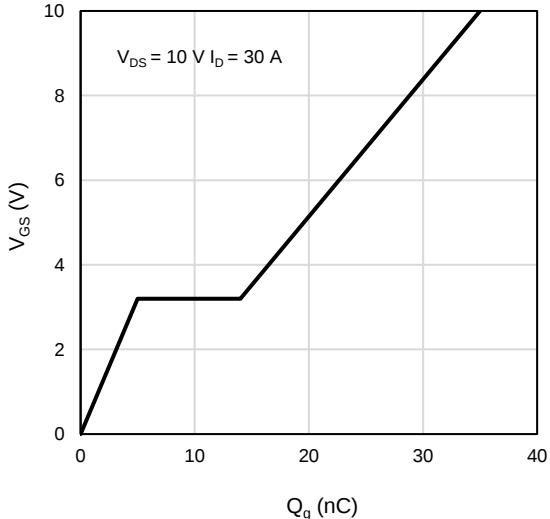


Figure 10: Gate-Charge Characteristics

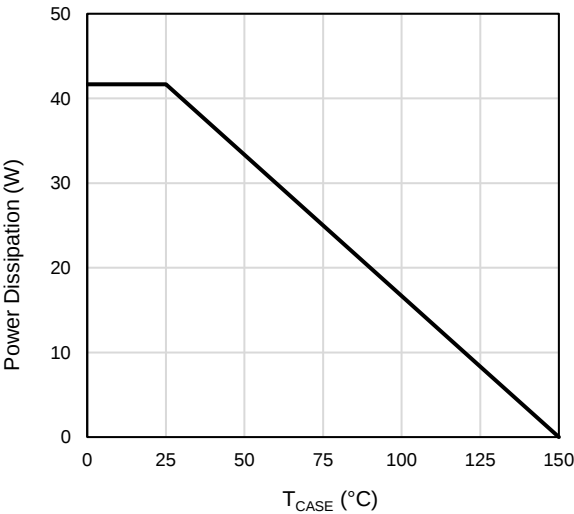


Figure 11: Power De-rating

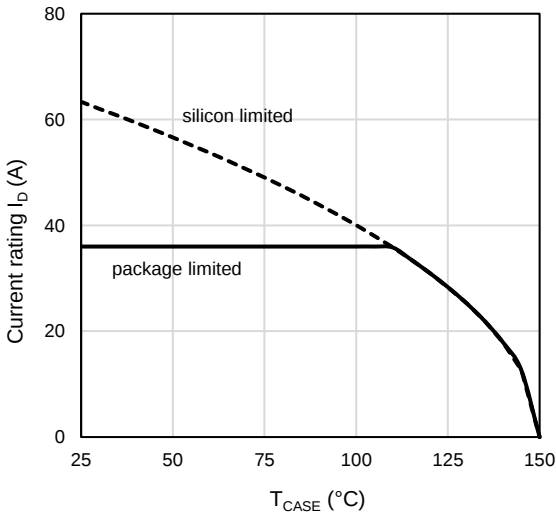


Figure 12: Current De-rating

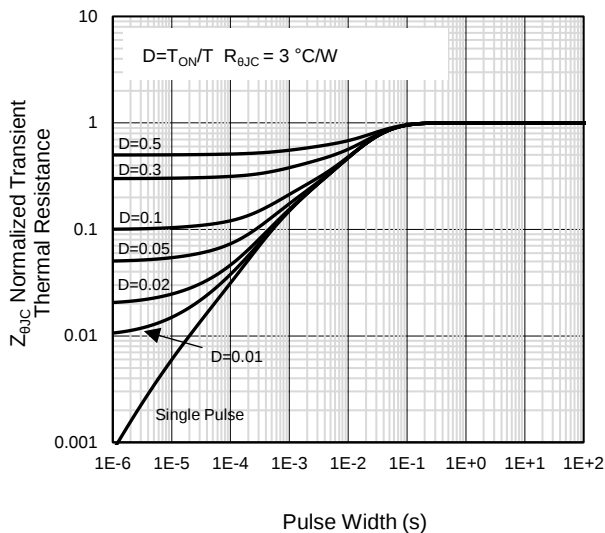


Figure 13: Normalized Maximum Transient Thermal Impedance

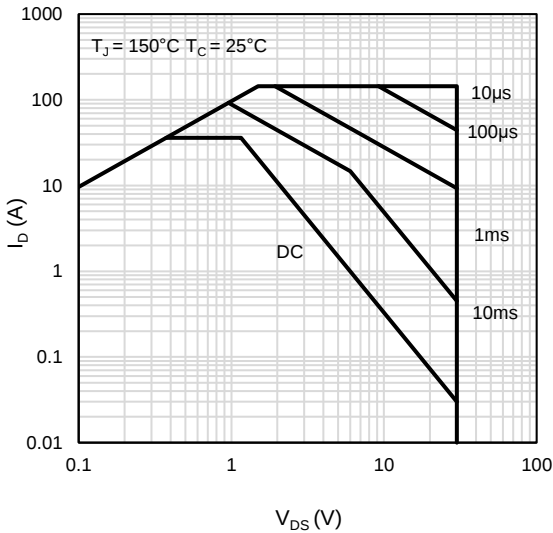
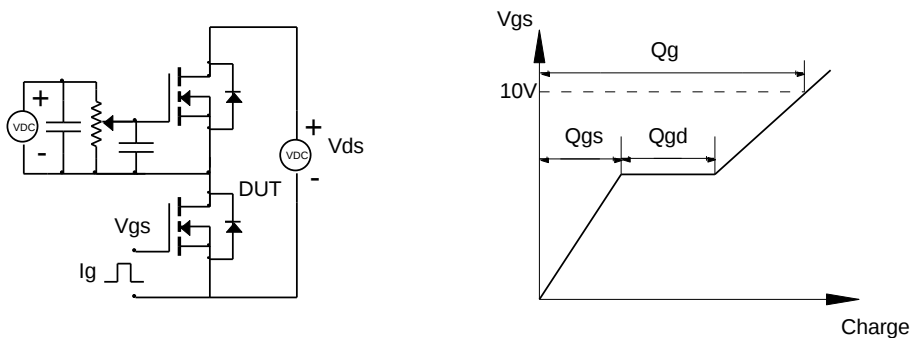


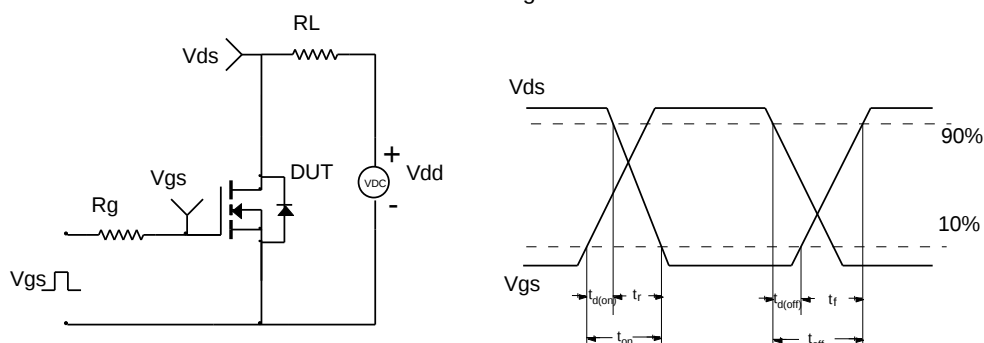
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

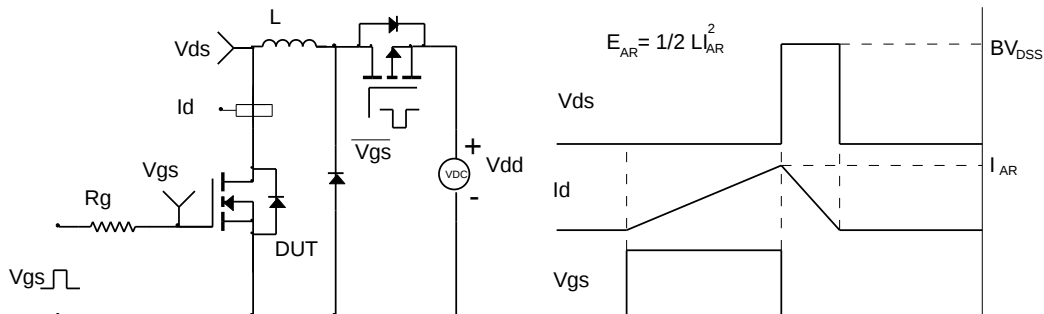
Gate Charge Test Circuit & Waveform



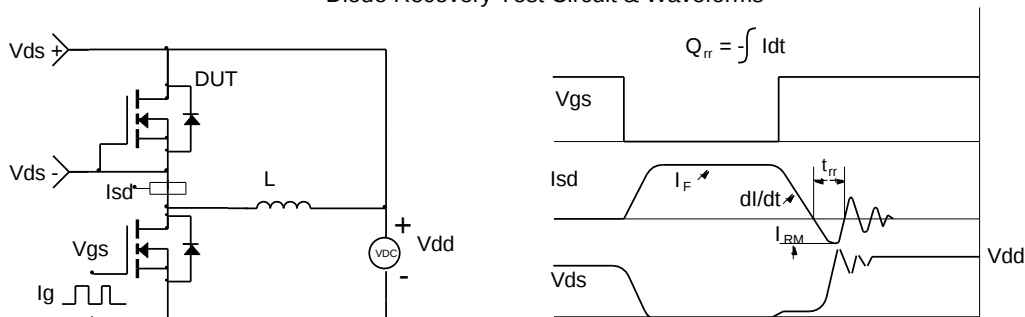
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Revision History

Revision	Released	Remark
Rev.1.2	2025	

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

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